

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

wherein the first shielding metal lines and the gate lines are in a first metal layer, and the first backup wires and the data lines are in a second metal layer.

2. (original) The flat panel display of claim 1, wherein the first backup wires extend from the data lines.

3. (original) The flat panel display of claim 2, wherein a plurality of overlap points of the first backup wires and the first shielding metal lines act as a plurality of repair points.

4. (canceled)

5. (currently amended) ~~The flat panel display of claim 2,~~ A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

wherein the first backup wires and the data lines are in a first metal layer, and the first shielding metal lines and the gate lines are in a second metal layer.

6. (original) The flat panel display of claim 1, wherein the first backup wires extend from the first shielding metal lines.

7. (original) The flat panel display of claim 6, wherein a plurality of overlap points of the first backup wires and the data lines act as a plurality of repair points.

8. (currently amended) ~~The flat panel display of claim 6,~~ A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

wherein the first backup wires, the first shielding metal lines and the gate lines are in a first metal layer, and the data lines are in a second metal layer.

9. (currently amended) ~~The flat panel display of claim 6,~~ A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

wherein the data lines are in a first metal layer, and the first backup wires, the first shielding metal lines and the gate lines are in a second metal layer.

10. (currently amended) ~~The flat panel display of claim 1,~~ A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

wherein the first backup wires are isolated from the data lines and the first shielding metal layer.

11. (original) The flat panel display of claim 10, wherein a plurality of overlap points of the first backup wires, the data lines, and the first shielding metal lines act as a plurality of repair points.

12. (original) The flat panel display of claim 10, wherein the first backup wires and the gate lines are in a first metal layer, and the first shielding metal layer and the data lines are in a second metal layer.

13. (original) The flat panel display of claim 10, wherein the first shielding metal layer and the data lines are in a first metal layer, and the first backup wires and the gate lines are in a second metal layer.

14. (currently amended) ~~The flat panel display of claim 1, further comprising:~~ A flat panel display allowing repair of data line defects, comprising:

a plurality of gate lines and a plurality of data lines intersecting to define a plurality of pixel regions;

a plurality of pixel electrodes disposed in the pixel regions respectively;

a plurality of first shielding metal lines disposed between the pixel electrodes and the data lines in the pixel regions respectively, each first shielding metal line separated from the others;

at least two first backup wires across the adjacent first shielding metal line and data line, wherein the first backup wires are isolated from the data lines or the first shielding metal lines or isolated from both of them;

a plurality of second shielding metal lines disposed between the pixel electrodes and the gate lines in the pixel regions respectively, each first shielding metal line separated from the others; and

at least two second backup wires across the adjacent second shielding metal line and gate line, ~~wherein the second backup wires are isolated from the gate lines or the second shielding metal lines or isolated from both of them;~~

wherein the second backup wires are isolated from the gate lines and the second shielding metal layer.

15. (original) The flat panel display of claim 14, wherein the second backup wires extend from the gate lines.

16. (original) The flat panel display of claim 15, wherein a plurality of overlap points of the second backup wires and the second shielding metal lines act as a plurality of repair points.

17. (original) The flat panel display of claim 14, wherein the second backup wires extend from the second shielding metal lines.

18. (original) The flat panel display of claim 17, wherein a plurality of overlap points of the second backup wires and the gate lines act as a plurality of repair points.

19. (canceled)

20. (currently amended) The flat panel display of claim ~~19~~14, wherein a plurality of overlap points of the second backup wires, the gate lines, and the second shielding metal lines act as a plurality of repair points.

21-22. (canceled)